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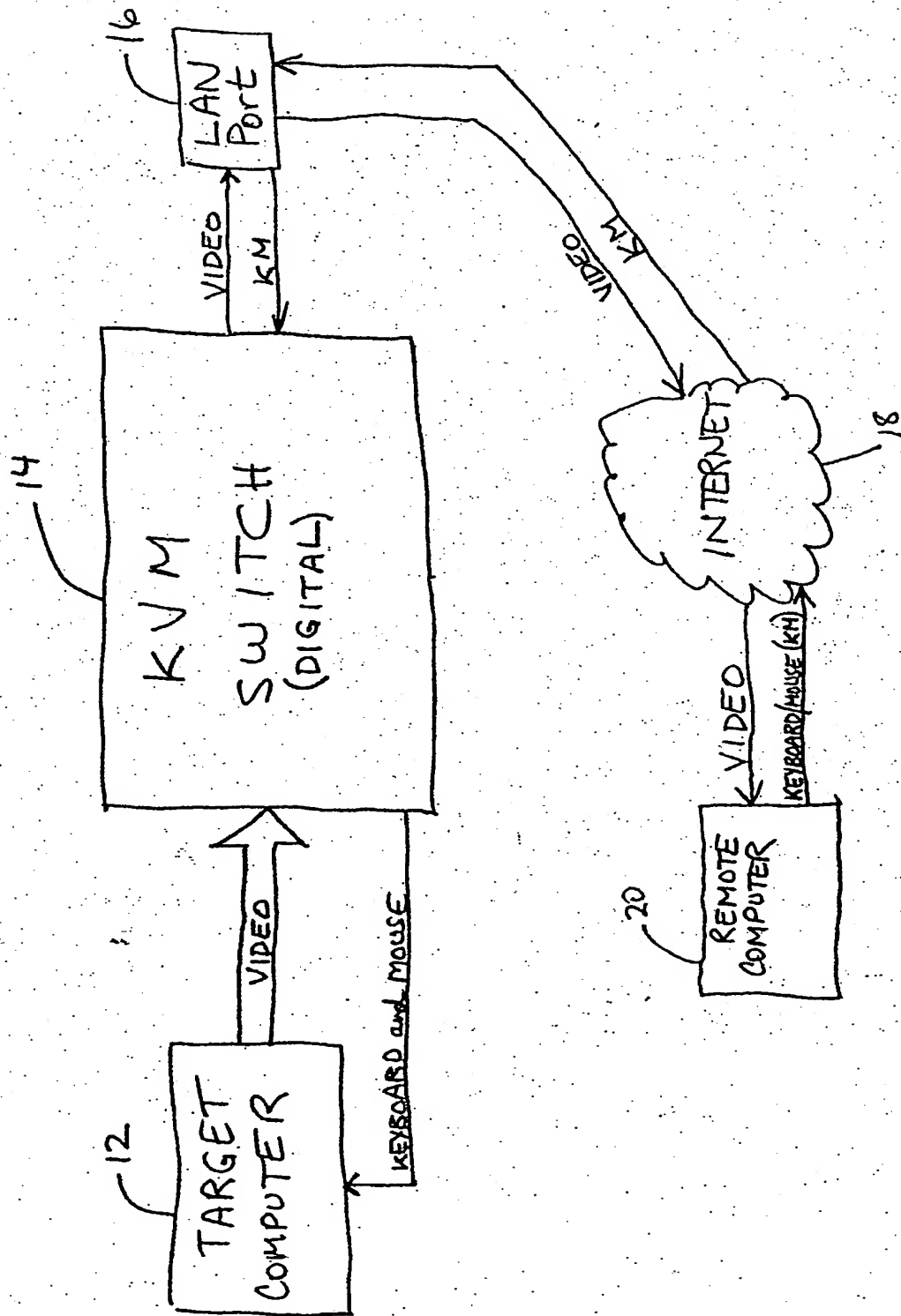


Figure 1

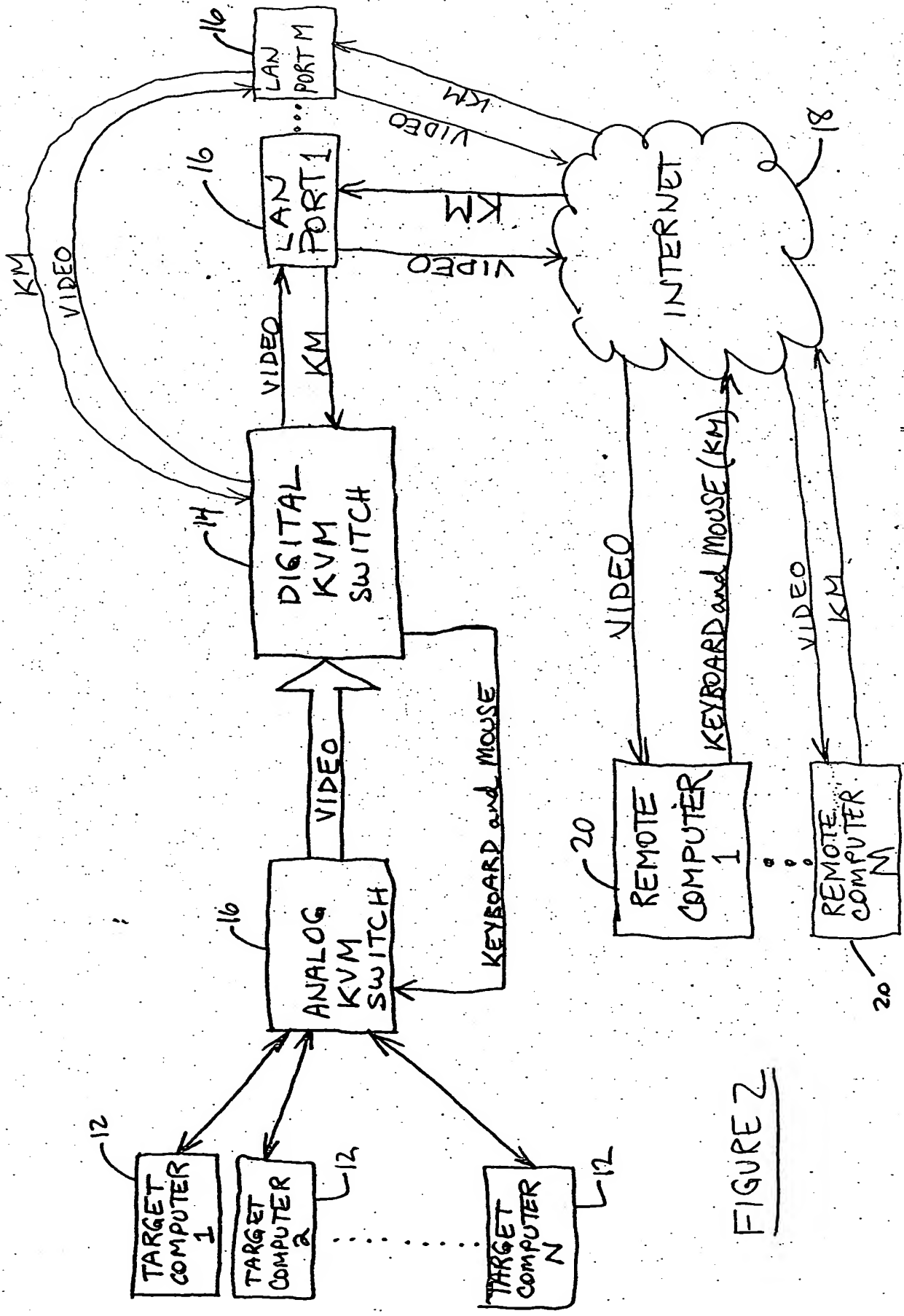


FIGURE 2

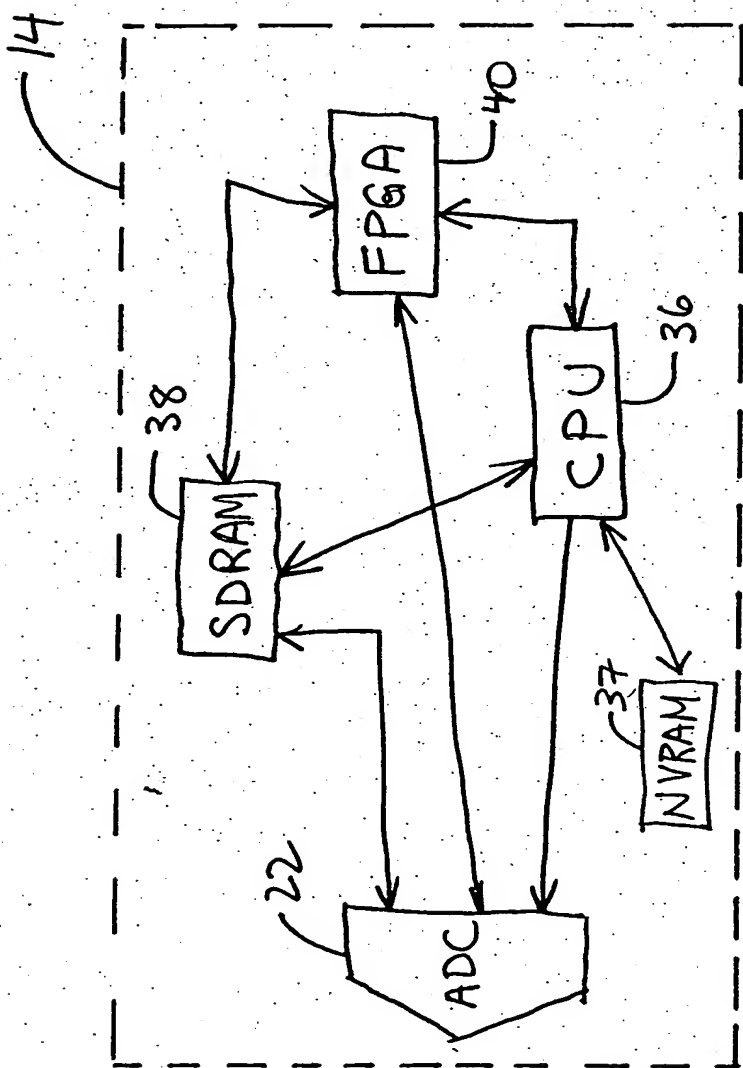
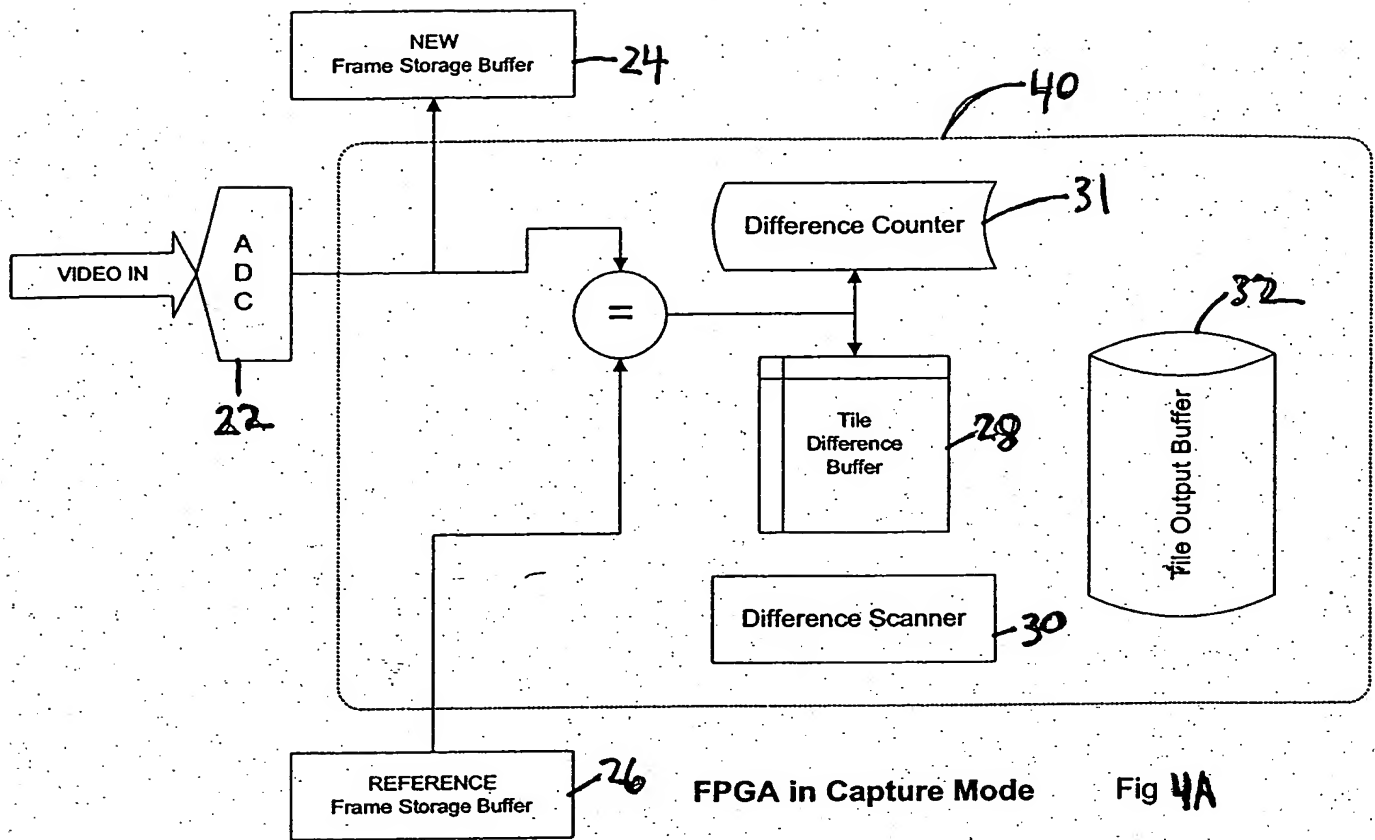
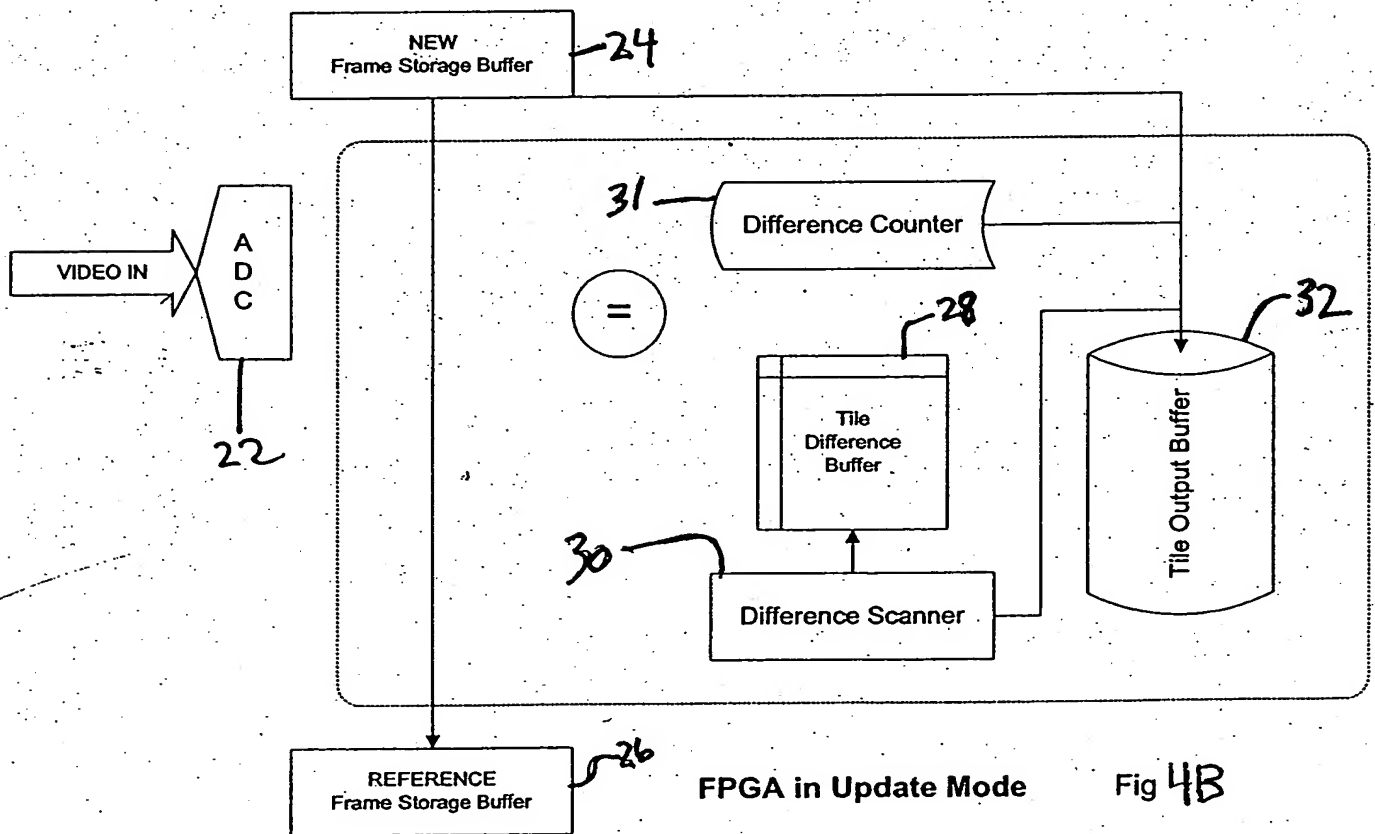


Figure 3



FPGA in Capture Mode

Fig 4A



FPGA in Update Mode

Fig 4B

FPGA Video Processing

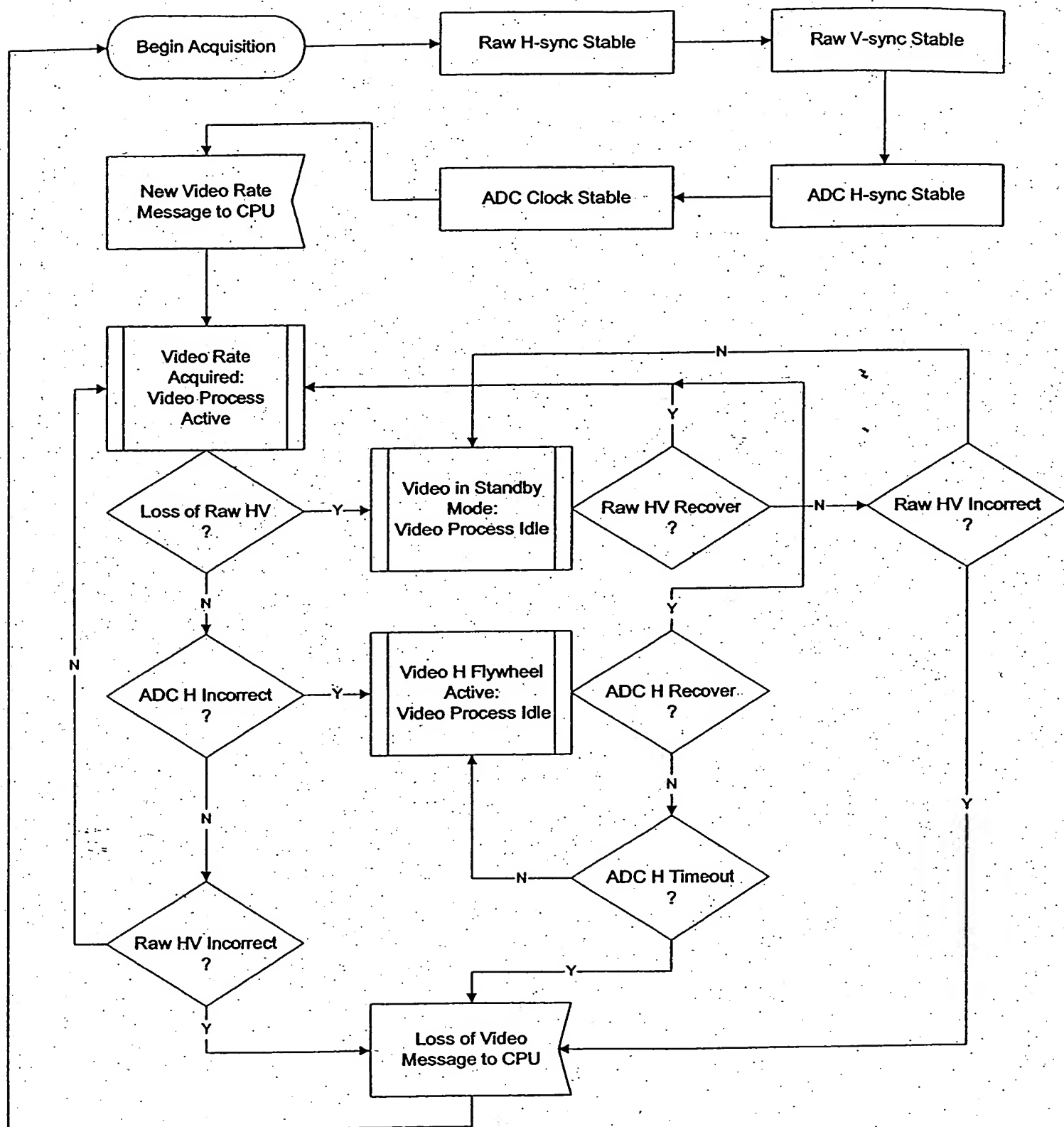


Figure 5

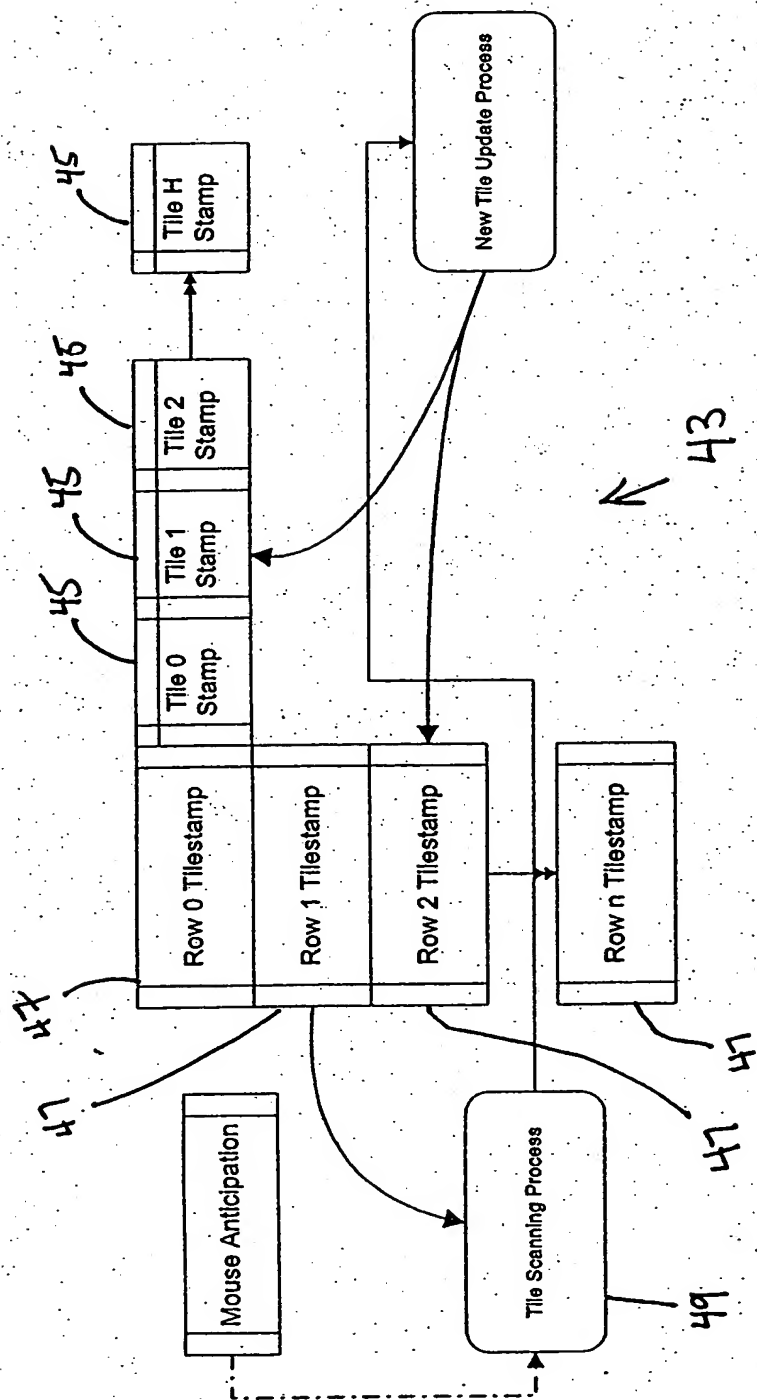


Figure 6

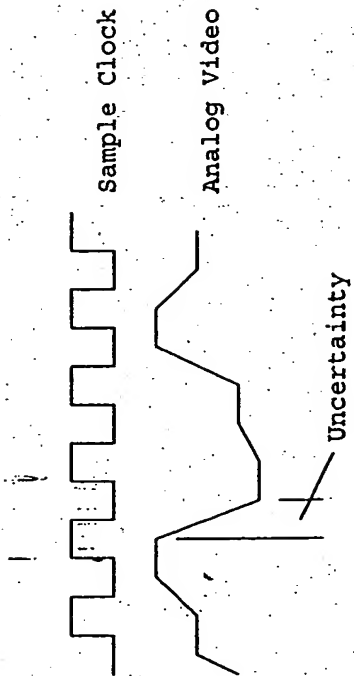
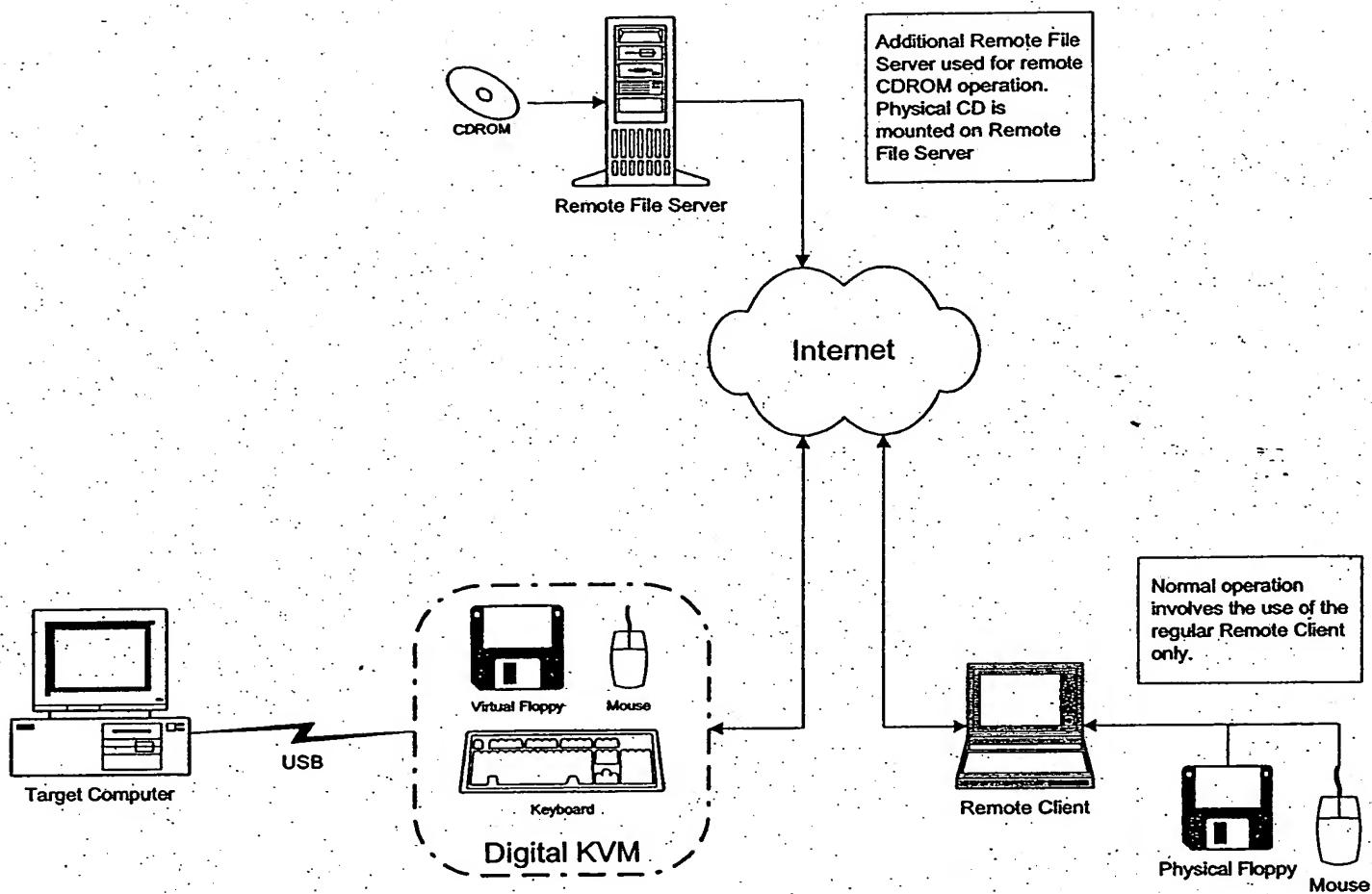


Figure 7



Figure 8



Virtual Combination Keyboard, Mouse, Floppy

Figure 10

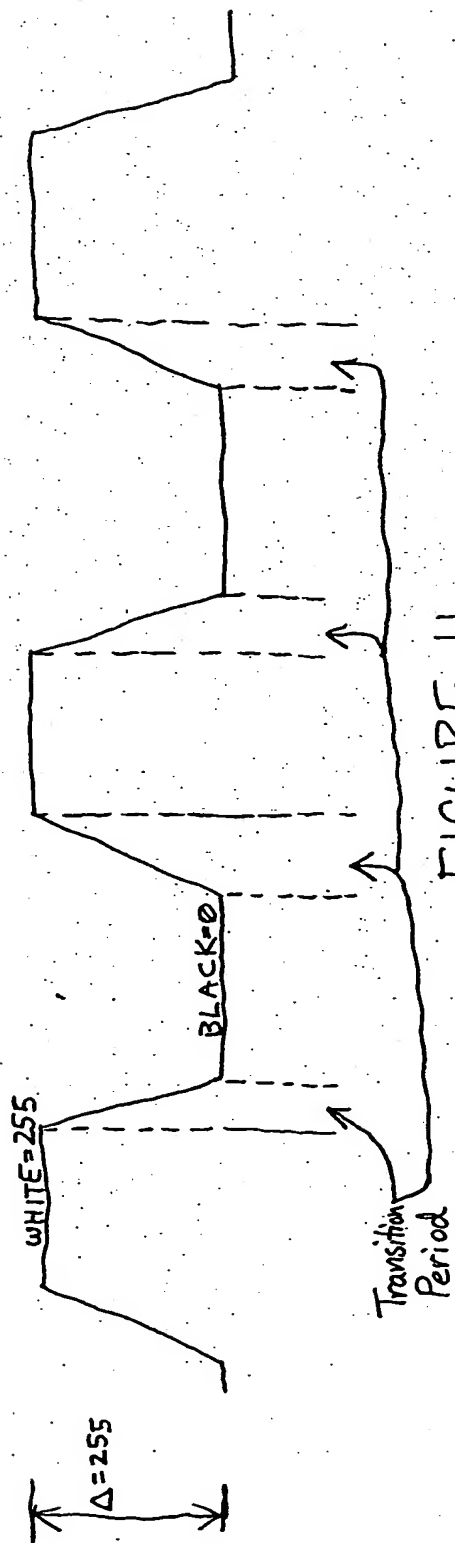


FIGURE II